

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	177	(345/209).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/25 10:17
S2	156431	"display device"	US-PGPUB; USPAT	OR	ON	2005/08/19 11:33
S3	39946	"driving circuit"	US-PGPUB; USPAT	OR	ON	2005/08/19 11:33
S4	9177	S2 and S3	US-PGPUB; USPAT	OR	ON	2005/08/19 11:33
S5	1078034	switch\$3	US-PGPUB; USPAT	OR	ON	2005/08/19 11:33
S6	7114	S4 and S5	US-PGPUB; USPAT	OR	ON	2005/08/19 11:33
S7	1581346	short\$3	US-PGPUB; USPAT	OR	ON	2005/08/19 11:34
S8	4197	S6 and S7	US-PGPUB; USPAT	OR	ON	2005/08/19 11:34
S9	133366	signal adj line	US-PGPUB; USPAT	OR	ON	2005/08/19 11:34
S10	1799	S8 and S9	US-PGPUB; USPAT	OR	ON	2005/08/19 11:35
S11	426922	matrix	US-PGPUB; USPAT	OR	ON	2005/08/19 11:35
S12	1566	S10 and S11	US-PGPUB; USPAT	OR	ON	2005/08/19 11:35
S13	526342	matrix or pixel	US-PGPUB; USPAT	OR	ON	2005/08/19 11:35
S14	1663	S10 and S13	US-PGPUB; USPAT	OR	ON	2005/08/19 11:35
S15	356610	transistor	US-PGPUB; USPAT	OR	ON	2005/08/19 11:36
S16	1466	S14 and S15	US-PGPUB; USPAT	OR	ON	2005/08/19 11:36
S17	836708	voltage	US-PGPUB; USPAT	OR	ON	2005/08/19 11:36
S18	1390	S16 and S17	US-PGPUB; USPAT	OR	ON	2005/08/19 11:37
S22	142	sarai.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/19 14:29
S23	40	sarai-osamu.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/19 14:29

S24	0	tanemure-fuminori.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/19 14:30
S25	12	tanemura-fuminori.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/19 14:30
S26	4	fujino-miki.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/19 14:30
S27	52	S23 or S25 or S26	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/19 14:46
S28	268	soneda-mitsuo.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/19 14:46
S29	83	kano-susumu.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/19 14:46
S30	8	taguma-michio.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/19 14:46
S31	14	iwamoto-katsuichi.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/19 14:47
S32	371	S28 or S29 or S30 or S31	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/19 14:47
S33	367289	lcd or "liquid crystal display"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/19 14:47
S34	30	S32 and S33	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/19 14:47
S35	36561	reduced same (power adj consumption)	US-PGPUB; USPAT	OR	ON	2005/08/19 14:59
S36	97624	lcd or "liquid crystal display"	US-PGPUB; USPAT	OR	ON	2005/08/19 14:59
S37	3544	S35 and S36	US-PGPUB; USPAT	OR	ON	2005/08/19 14:59

S38	102407	fet or "field effect transistor"	US-PGPUB; USPAT	OR	ON	2005/08/19 15:00
S39	376	S37 and S38	US-PGPUB; USPAT	OR	ON	2005/08/19 15:00
S40	39946	driving adj circuit	US-PGPUB; USPAT	OR	ON	2005/08/19 15:00
S41	92	S39 and S40	US-PGPUB; USPAT	OR	ON	2005/08/19 15:00
S42	152469	lcd or "liquid crystal display"	US-PGPUB; USPAT	OR	ON	2005/08/22 07:40
S43	367816	lcd or "liquid crystal display"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/22 07:40
S44	14	"dot reversing"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/22 07:40
S45	920	"dot inversion"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/22 07:41
S46	880	S43 and (S44 or S45)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/22 07:41
S47	3820974	driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/22 07:41
S48	857	S46 and S47	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/22 07:42
S49	1859700	switch	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/22 07:42
S50	339	S48 and S49	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/22 07:42
S51	2467883	switch\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/22 07:42
S52	651	S48 and S51	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/22 07:42

S53	2250	(345/204,208).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/25 10:17
S54	153107	lcd or "liquid crystal display"	US-PGPUB; USPAT	OR	ON	2005/08/25 10:18
S55	1420	S53 and S54	US-PGPUB; USPAT	OR	ON	2005/08/25 10:18
S56	102653	fet or "field effect transistor"	US-PGPUB; USPAT	OR	ON	2005/08/25 10:19
S57	143	S55 and S56	US-PGPUB; USPAT	OR	ON	2005/08/25 10:19
S58	3586	(345/87,98,100).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/25 10:44
S59	2002	(345/87).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/25 11:21
S60	1853	(345/98,100).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/25 10:44
S61	102653	fet or "field effect transistor"	US-PGPUB; USPAT	OR	ON	2005/08/25 11:21
S62	355	S58 and S61	US-PGPUB; USPAT	OR	ON	2005/08/25 11:21